



**SYDERAL Polska** is a Polish SME established in 2016 providing electronics and software solutions for the space sector.

The mission of **SYDERAL Polska** is delivering high quality products while maintaining the openness for applying innovative solutions within the space technology domain. The electronics and software for our clients are designed by first-rate Polish engineers with experience in the space sector.

In order to reinforce our team, we are searching for an:

### **FPGA ENGINEER**

As an FPGA Engineer you will participate in exciting projects carried out for the space industry. You will become a vital part of #syderalpolska – a great team of colleagues and friends.

We are looking for you if you have:

- over 4 years of experience with FPGAs in the following areas: design, RTL coding, verification and hardware debugging
- good VHDL or Verilog RTL design skills
- experience with FPGA synthesis and implementation tools, such as Synopsys Synplify, Xilinx Vivado, Microsemi Libero
- experience with FPGA simulation tools, such as Mentor Modelsim/Quarta or Aldec Riviera-PRO/Active-HDL
- experience with FPGA debugging tools, like Xilinx Chipscope or Synopsys Identify
- knowledge of UVM/OSM or UVVM/OSVVM verification methods.

### **We offer**

A job in highly fascinating environment of space industry, as well as friendly work environment, trainings plus competitive salary adequate to skills and experience.

Are you interested and motivated to join our team? Please send your CV to:

[info@syderal.pl](mailto:info@syderal.pl)